By Thomas Bonatti

On behalf of: Josh, Ben Effron, Jeff, Thomas

Jeff and Josh proposed Accumulator with 3 extra registers plus design, and we agreed to use that.

Agreed to use a git-hub repo

Ben proposed a pipeline design, but we decided to analyses our design goals before deciding.

DISCUSSION OF OPPERATIONS

Agreed to use a main register and a move command to save space in instructions by not addressing registers.

**Addressable Registers**

|  |  |  |  |
| --- | --- | --- | --- |
| **Register** | **Purpose** | **Register** | **Purpose** |
| $M | Main accumulator | $t0 | General use |
| $ra | Return address | $t1 | General use |
| $sp | Stack pointer | $s0 | General use |
| $at | Pseudo instructions | $s1 | Safe use |

**Non-Addressable Registers:**

|  |  |  |  |
| --- | --- | --- | --- |
| **Register** | **Purpose** | **Register** | **Purpose** |
| PC | Points to current instruction. | ALUout |  |
|  |  |  |  |

**I-Type instructions (Immediate):**

I-type instructions will use the main accumulator register, and a 12 bit immediate. Most logic and arithmetic commands will use this.

|  |  |
| --- | --- |
| Op Code [15:12] | Immediate [11:0] |

**R-type (move):**

Operations using registers

|  |  |  |  |
| --- | --- | --- | --- |
| Op Code [15:12] | Function code [11:6] | rt (register) [5:3] | rs (register) [2:0] |

**IR-Type(Immediate and Register):**

Branches and load word store word

|  |  |  |
| --- | --- | --- |
| Op code [15:12] | Reg [11:9] | Immediate [8:0] |

**JR-Type [Jump Register]:**

Used for jump register command

|  |  |  |  |
| --- | --- | --- | --- |
| Op Code [15:12] | Func Code [11:8] | Reg [7:5] | Unused [4:0] |

Op codes:

|  |  |  |  |
| --- | --- | --- | --- |
| **Op Code** | **Operation** | **Op Code** | **Operation** |
| 0000 | Use function code\* | 1000 | li (load immediate) |
| 0001 | addi (add immediate) | 1001 | ori (or immediate) |
| 0010 | beq (brach equal) | 1010 | andi (and immediate) |
| 0011 | bne (branch not equal) | 1011 | nori (nor immediate) |
| 0100 | J (jump) | 1100 | sll (shift left logical) |
| 0101 | lw (load word) | 1101 | srl (shift right logical) |
| 0110 | sw (store word) | 1110 | sra (shift right arithmetic) |
| 0111 | lui (load upper immediate) | 1111 | slt (set less than) |

Function Codes:

|  |  |  |  |
| --- | --- | --- | --- |
| **Func Code** | **Operation** | **Func Code** | **Operation** |
| 0000 | add | 1000 | move |
| 0001 | and (logic) | 1001 |  |
| 0010 | or (logic) | 1010 |  |
| 0011 | xor (logic) | 1011 |  |
| 0100 | nor (logic) | 1100 |  |
| 0101 | swap | 1101 |  |
| 0110 | copy | 1110 |  |
| 0111 | jr | 1111 |  |

**Operation Descriptions**

**Check function code** (op = 0000)

If the op code is 0000, the operation to be executed will be determiner by a 4 bit function code [11:8]

**Addi** **– add immediate** (op 0001)

signature: addi <12 bit imm.>

Addi is an I-type operation that adds the given immediate to the accumulator register.

**Beq – Branch equal**  (op = 0010)

signature: beq $r, <9 bit imm.>

Beq branches if the given register $r and the main accumulator are equal. Can branch +/- 256 lines up or down.

**Bne** - **brach not equal**(op = 0011)   
signature: beq $r, <9 bit imm.>

Bne branches if the given register $r and the main accumulator are not equal. Can branch +/- 256 lines up or down.

**J – jump** (op = 0100)

Signature: j <12 bit imm.>

We have yet to fully designate how this command will perform a jump.

**lw – load word** (op = 0101)

signature: lw $r[ <9 bit imm.> ]

Loads a 16 bit chunk of data from memory at the given address incremented by the immediate.

**sw** **– store word** (op = 0110)

signature: sw $r [ <9 bit imm.> ]

Stores the value in the main register in memory at the given address, incremented by the immediate.

**Li – Load Immediate** (op = 1000)

Signature: li <12 bit imm.>

Li stores the given immediate value in the main register.

**ori – or immediate** (op = 1001)

signature: ori <12 bit imm.>

ori performs a logical or operation on the value of the main register and the immediate, then stores the value in the main register.

**andi – and immediate** (op = 1010)

signature: andi <12 bit imm.>

andi performs a logical and operation on the value of the main register and the immediate, then stores the value in the main register.

**nori – nor immediate** (op = 1011)

signature: nori <12 bit imm.>

nori performs a logical nor operation on the value of the main register and the immediate, then stores the value in the main register.

**sll – shift left logical** (op = 1100)

signature: sll $r, <9 bit imm.>

**srl – shift right logical** (op = 1101)

signature: srl $r, <9 bit imm.>

**sra – shift right arithmetic** (op = 1110)

signature: sra $r, <9 bit imm.>

**slt – set less than** (op = 1111)

signature: slt $r, <9 bit imm.>

**add – add** (op = 0000, func = 0000)

signature: add $r1, $r2

Add adds $r1 and $r2 together and stores in the main register. To accumulate, use add $m, $r1.

**and – and** (op = 0000, func = 0001)

signature: and $r1, $r2

Preforms a logical and operation on two registers, then stores the result in the main register.

**or – or** (op = 0000, func = 0010)

signature: or $r1, $r2

Preforms a logical or operation on two registers, then stores the result in the main register.

**xor – xor** (op = 0000, func = 0011)

signature: xor $r1, $r2

Preforms a logical xor operation on two registers, then stores the result in the main register.

**nor – nor** (op = 0000, func = 0100)

signature: nor $r1, $r2

Preforms a logical nor operation on two registers, then stores the result in the main register.

**swap – swap** (op = 0000, func = 0101)

signature: swap $r1, $r2

Swaps the values in $r1 and $r2.

**copy – copy** (op = 0000, func = 0110)

signature: copy $r1, $r2

Writes the value of $r1 into register $r2.

**jr – jump register** (op = 0000, func = 0111)

signature: jr $r

jumps to the address in the given register.

**move – move** (op = 0000, func = 1000)

signature: move $r1, $r2

Writes the value of $r1 to $r2, then sets the value of $r1 to zero.

**sltr – set less than register** (op = 0000, func = 1001)

signature: sltr $r1, $r2

If the main register is less than register $r1, a flag is set to 1 in register $r2. If the main register is equal to or greater than register $r1, a flag is set to 0 in register $r2.